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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10061546	FILING DATE 02/01/2002	CLASS 257174	SUBCLASS 262	GAU 2827 2847	EXAMINER NIRALIS
**APPLICANTS: Sun Xiao-Peng; Wang Nanlei;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 09/675,086 09/28/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				017829-000710US	
Verified and Acknowledged Examiners's initials					
TITLE : Multi-layer interconnect module and method of interconnection					
U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner			
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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